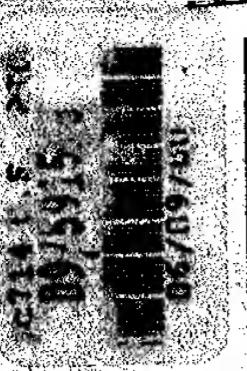


		Subclass	Class	ISSUE CLASSIFICATION	
U.S. UTILITY Patent Application					
 O.I.P.E. FF			PATENT DATE AA		
SCANNED <u>AT (1)</u> Q.A. <u>AA</u>					
APPLICATION NO. 09/591523		CONT/PRIOR D F	CLASS 430 174	SUBCLASS 110	ART UNIT 1756 284
EXAMINER <u>Nohi</u> <u>CHNGO</u>					
APPLICANTS Fujio KUWAKO					
TITLE #1/8 Method for producing multilayer printed circuit boards having blind vias					
27					
PATENT NUMBER					

ISSUING CLASSIFICATION

<input type="checkbox"/> TERMINAL DISCLAIMER	DRAWINGS			CLAIMS ALLOWED	
	Sheets Drwg.	Figs. Drwg.	Print Fig.	Total Claims	Print Claim for O.G.
<input type="checkbox"/> The term of this patent subsequent to _____ (date) has been disclaimed.				NOTICE OF ALLOWANCE MAILED	
<input type="checkbox"/> The term of this patent shall not extend beyond the expiration date of U.S Patent. No. _____				ISSUE FEE	
<input type="checkbox"/> The terminal _____ months of this patent have been disclaimed.				Amount Due	Date Paid
				ISSUE BATCH NUMBER	

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